

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,543,133 B1  
APPLICATION NO. : 10/643585  
DATED : June 2, 2009  
INVENTOR(S) : Steven L. Scott

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page; item(s) (56);

On page 2, in column 2, under "Other Publications", line 8, delete "verison" and insert -- version --, therefor.

On page 3, in column 1, under "Other Publications", line 16, delete "publsihers," and insert -- publishers, --, therefor.

On page 4, in column 1, under "Other Publications", line 72, delete "of" and insert -- on --, therefor.

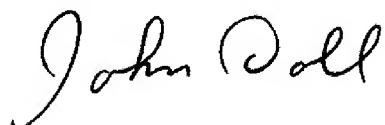
On page 4, in column 2, under "Other Publications", line 1, delete "in" and insert -- into --, therefor.

In column 4, line 63, after "node." insert -- If the virtual node corresponds to the local node, then the method includes translating the virtual memory address into a local physical memory address on the local node. --.

In column 6, line 5, delete "description" and insert -- descriptions --, therefor.

Signed and Sealed this

Fourth Day of August, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*